

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of: )  
)  
Takayuki ITO )  
) Group Art Unit: Not assigned  
Application No.: Not Yet Assigned )  
) Examiner: Not assigned  
Filed: October 28, 2003 )  
)  
For: SEMICONDUCTOR DEVICE )  
AND METHOD OF )  
MANUFACTURING THE SAME )

**Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450**

Sir:

**INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97(b)**

Pursuant to 37 C.F.R. §§1.56 and 1.97(b), applicant brings to the Examiner's attention the documents listed on attached Form PTO-1449. Copies of the listed documents are attached. Applicant respectfully requests that the Examiner consider the documents listed on attached Form PTO-1449 and indicate that they were considered by making an appropriate notation on this form.

This Information Disclosure Statement is being filed with the above-referenced application.

The following is a concise statement of relevance of the non-English language documents:

1. Japanese Patent Application No. 2002-141298 discloses an activation method for impurities in Si by flash lamp annealing.

FINNEGAN  
HENDERSON  
FARABOW  
GARRETT &  
DUNNER LLP

1300 I Street, NW  
Washington, DC 20005  
202.408.4000  
Fax 202.408.4400  
www.finnegan.com

2. Japanese Patent Application No. 2002-246328 discloses the uniform improvement of the illumination of a flash lamp.

The following are listed on the accompanying PTO-1449 and are in a non-English language:

- 3. Japanese Patent Application No. 10-26772
- 4. Japanese Patent Application No. 2002-175772
- 5. Japanese Patent Application No. 2002-198322

The relevance of documents 1 - 5 is discussed at page 2 of the specification of the present application.

This submission does not represent that a search has been made or that no better art exists and does not constitute an admission that each or all of the listed documents are material or constitute "prior art." If the Examiner applies any of the documents as prior art against any claim in the application and applicant determines that the cited documents do not constitute "prior art" under United States law, applicant reserves the right to present to the office the relevant facts and law regarding the appropriate status of such documents. Applicant further reserves the right to take appropriate action to establish the patentability of the disclosed invention over the listed documents, should one or more of the documents be applied against the claims of the present application.

If there is any fee due in connection with the filing of this Statement, please

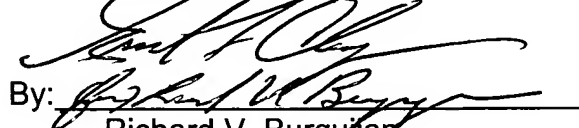
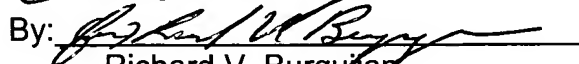
FINNEGAN  
HENDERSON  
FARABOW  
GARRETT &  
DUNNER LLP

1300 I Street, NW  
Washington, DC 20005  
202.408.4000  
Fax 202.408.4400  
www.finnegan.com

charge the fee to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,  
GARRETT & DUNNER, L.L.P.

  
By: 

Richard V. Burgujian  
Reg. No. 31,744

Dated: October 28, 2003

Enclosures  
RVB/FPD/dvg

ERNEST F. CHAPMAN  
Reg. No. 25,961

FINNEGAN  
HENDERSON  
FARABOW  
GARRETT &  
DUNNER LLP

1300 I Street, NW  
Washington, DC 20005  
202.408.4000  
Fax 202.408.4400  
[www.finnegan.com](http://www.finnegan.com)

# INFORMATION DISCLOSURE CITATION

Atty. Docket No.	04329.3167	Application No.	
Applicant	Takayuki ITO		
Filing Date	October 28, 2003	Group:	Not assigned

U.S. PATENT DOCUMENTS							
Examiner Initial*		Document Number	Issue Date	Name	Class	Sub Class	Filing Date If Appropriate
		6,417,515 B1	7/9/02	Barrett et al.			

FOREIGN PATENT DOCUMENTS							
		Document Number	Publication Date	Country	Class	Sub Class	Translation Yes or No
		2002-198322	7/12/02	Japan			No
		2002-175772	6/21/02	Japan			No
		10-26772	1/27/98	Japan			No
		2002-141298	5/17/02	Japan			No
		2002-246328	8/30/02	Japan			No

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)	
	Takayuki I., "SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING THE SAME", U.S. Serial No. 10/238,642, filed September 11, 2002.

Examiner	Date Considered
*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	
Form PTO 1449	Patent and Trademark Office - U.S. Department of Commerce